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7590

08/12/2004

Greg J. Michelson
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Tina Kavanaugh	(Depositor's name)
<i>Tina Kavanaugh</i>	(Signature)
9-14-04	(Date)

09/15/2004 RMEBRAH1 00000170 502257 10687468

01 FC:1501 1330.00 DA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/687,468	10/15/2003	Edward A. Ramsden	M-15273 US	8863

TITLE OF INVENTION: ANALOG-TO-DIGITAL CONVERTERS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	11/12/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
WILLIAMS, HOWARD L	2819	341-155000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,

(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 MacPherson Kwok Chen

X & Heid LLP

2 Greg J. Michelson

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Lattice Semiconductor Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

☒ Issue Fee☐ Publication Fee (No small entity discount permitted)☐ Advance Order - # of Copies

4b. Payment of Fee(s):

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is not claiming SMALL ENTITY status. See, e.g., 37 CFR 1.27(g)(2).

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(Authorized Signature) Greg J. Michelson (Date) 9-14-04
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Edward A. Ramsden
Assignee: Lattice Semiconductor Corporation
Title: ANALOG-TO-DIGITAL CONVERTERS
Serial No.: 10/687,468 Filing Date: October 15, 2003
Examiner: Williams, Howard L. Group Art Unit: 2819
Docket No.: M-15273 US

Irvine, California
September 14, 2004

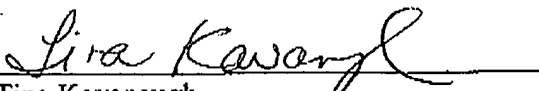
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ISSUE FEE TRANSMITTAL**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following Issue Fee Transmittal (1 page) and Fee Address Indication Form (1 page) are being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: September 14, 2004


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